

Applicant: **ODAKE, YOSHINORI, et al.**Docket No. **60188-402**Title: **METHOD OF FABRICATING NONVOLATILE SEMICONDUCTOR MEMORY DEVICE**Serial/Reg./Patent No. **09/987,001**Date Sent: **5/22/2003**☒ Hand Carried☐ Fax☐ Electronic☐ Cert. of Mailing☐ Express Mail No.☒ Transmittal Letter

New Patent App

☐ Utility☐ Design☐ Cont.☐ CIP☐ Div.☐ PCT☐ CPA☐ RCE☐ Prov☒ Other: **Power of Attorney**

_____ pages of Specification

_____ pages of Claims

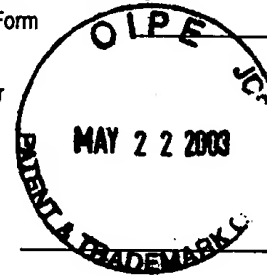
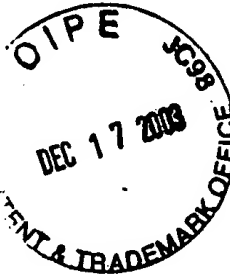
_____ pages of Abstract

_____ pages of Formal/Informal Drawings

☐ Small Entity☐ Large Entity☐ Declaration/Power of Attorney☐ Recordation of Assignment/Security Agreement☐ Information Disclosure Statement

_____ Form PTO 1449

_____ copies of cited references

☐ Preliminary Amendment☐ Response to Missing Parts Notice☐ Resp. to Notice to Correct App. Papers☐ Certified Copy of Priority Doc.☐ Claim for Convention Priority☐ Response/Amendment to Office Action of _____☐ Request for _____ month Extension of Time☐ Letter submitting _____ pages of drawings☐ Req. for Approval of Drawing Amendments☐ Req. for Oral Hearing☐ Not. of Appeal☐ Appeal Brief☐ Reply Brief☐ Rule 312 Amendment/Letter☐ Req. for Acknowledgement of Cited Art☐ Issue Fee☐ Publication Fee☐ Req. for Certificate of Correction☐ Maintenance Fee for _____ years after grant☐ Fee Address Indication Form☐ Terminal Disclaimer☐ Petition to Commissioner☐ Status Inquiry☐ Other

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CMS Descrip.: _____

THE PATENT AND TRADEMARK OFFICE DATE STAMPED HEREON IS ACKNOWLEDGEMENT THAT THE ITEMS, CHECKED ABOVE, WERE RECEIVED BY THE PTO ON THE DATE STAMPED.

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of

Yoshinori ODAKE, et al.

Serial No.: 09/987,001

Filed: November 13, 2001

For: METHOD OF FABRICATING NONVOLATILE SEMICONDUCTOR MEMORY...



Group Art Unit: 1812

Examiner: Unknown

**TRANSMITTAL OF REVOCATION OF POWER OF ATTORNEY
AND REQUEST FOR CHANGE OF ADDRESS**

Commissioner for Patents
P. O. Box 1450
Alexandria, VA 22313-1450

Sir:

Enclosed is a Revocation of Power of Attorney, Appointment and Certification Under 37 CFR 3.373(B) for the above referenced application.

Please change the USPTO records to reflect our Docket Number as noted above.

Also, please address all future correspondence to:

McDermott, Will & Emery
600 13th Street, N.W.
Washington, D.C. 20005-3096



Respectfully submitted,

McDERMOTT, WILL & EMERY

Dated: 5/22/03

By: 

Michael E. Fogarty
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Docket No.: 740819-689



PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of

Yoshinori ODAKE, Fumihiko NORO, Takahiko HASHIDZUME

Serial No.: 09/987,001

Filed: November 13, 2001

For: METHOD OF FABRICATING NONVOLATILE SEMICONDUCTOR MEMORY
DEVICE

**REVOCATION OF POWER OF ATTORNEY, APPOINTMENT
AND CERTIFICATION UNDER 37 CFR 3.73(B)**

Commissioner for Patents
Washington, DC 20231

Sir:

The undersigned assignee of the above-identified application hereby revokes all previous Powers of Attorney and appoints the following attorneys with full power to prosecute the application, to make alterations and amendments therein, and to transact all business in the United States Patent Office connected therewith.

I hereby appoint the following attorney(s) and/or agent(s): Steven W. Allis, Reg. No. 50,532; Stephen A. Becker, Reg. No. 26,527; John G. Bisbikis, Reg. No. 37,095; Daniel Bucca, Reg. No. 42,368; Kenneth L. Cage, Reg. No. 26,151; Jennifer Chen, Reg. No. 42,404; Bernard P. Codd, Reg. No. 46,429; Lawrence T. Cullen, Reg. No. 44,489; Paul Devinsky, Reg. No. 28,553; Margaret M. Duncan, Reg. No. 30,879; Shamita De. Etienne-Cummings, Reg. No. 46,072; Ramyar M. Farid, Reg. No. 46,692; Brian E. Ferguson, Reg. No. 36,801; Michael E. Fogarty, Reg. No. 36,139; John R. Fuisz, Reg. No. 37,327; Willem F. Gadiano, Reg. No. 37,136; Keith E. George, Reg. No. 34,111; John A. Hankins, Reg. No. 32,029; Eric J. Kraus, Reg. No. 36,190; Catherine Krupka, Reg. No. 46,227; Jack Q. Lever, Reg. No. 28,149; Raphael V. Lupo, Reg. No. 28,363; Burman Y. Mathis III, Reg. No. 44,907; Michael A. Messina, Reg. No. 33,424; Dawn L. Palmer, Reg. No. 41,238; Joseph H. Paquin, Jr., Reg. No. 31,647; Scott D. Paul, Reg. No. 42,984; William D. Pegg, Reg. No. 42,988; Robert L. Price, Reg. No. 22,685; Gene Z. Robinson, Reg. No. 33,351; Brian K. Seidleck, Reg. No. 51,321; Joy Ann G. Serauskas, Reg. No. 27,952; Jiri F. Smetana, Reg. No. 52,456; David A. Spenard, Reg. No. 37,449; Arthur J. Steiner, Reg. No. 26,106; Wesley Strickland, Reg. No. 44,363;

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Michael D. Switzer, Reg. No. 39,552; David M. Tennant, Reg. No. 48,362; Judith L. Toffenetti, Reg. No. 39,048; Daniel S. Trainor, Reg. No. 43,959; Shival P. Virmani, Reg. No. 45,032; Kelli N. Watson, Reg. No. 47,170; Cameron K. Weiffenbach, Reg. No. 44,488; Aaron Weisstuch, Reg. No. 41,557; Edward J. Wise, Reg. No. 34,523; Jeffrey A. Woller, Reg. No. 48,041; Alexander V. Yampolsky, Reg. No. 36,324; Robert W. Zelnick, Reg. No. 36,976; and Wei-Chen Chen, admitted under 37 CFR 10.9(b) all of

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with full power of substitution and revocation, to prosecute this application and to transact all business in the Patent and Trademark Office connected therewith, and all future correspondence should be addressed to them.

CERTIFICATE UNDER 37 CFR 3.73(b)

MATSUSHITA ELECTRIC INDUSTRIAL CO., LTD., a corporation of 1006, Oaza Kadoma, Kadoma-shi, Osaka 571-8501, Japan, certifies that it is the assignee of the entire right, title and interest in the patent application identified above by virtue of:

An Assignment from the inventor(s) of the patent application identified above. The assignment was recorded in the Patent and Trademark Office at Reel 012628, Frame 0990.

The undersigned has reviewed all the documents in the chain of title of the patent application identified above and, to the best of undersigned's knowledge and belief, title is in the assignee identified above.

The undersigned (whose title is supplied below) is empowered to act on behalf of the assignee.

The undersigned further declares that all statements made herein of its own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States

Serial No.: 09/987,001

Code, and that such willful false statements may jeopardize the validity of the application or any patent issuing thereon.

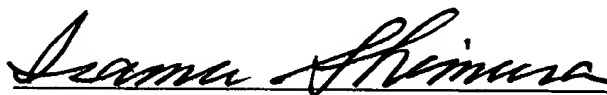
Respectfully submitted,

MATSUSHITA ELECTRIC INDUSTRIAL CO., LTD.

Name: Isamu SHIMURA

Title: Director, IP Development Center
Authorized Signing Officer

Signature:



Date:

04.17.2003